

**APPLICATION**

**FOR**

**UNITED STATES LETTERS PATENT**

**TITLE:           IMPLANTING CARBON TO FORM P-TYPE  
SOURCE DRAIN EXTENSIONS**

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Express Mail No. EL 990 136 009 US

Date: February 26, 2004

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SOURCE DRAIN EXTENSIONS

Background

This invention relates generally to the fabrication of  
5 integrated circuits.

In the fabrication of integrated circuits, a gate  
electrode may be utilized as a mask for forming source and  
drain junctions. The source and drain junctions may  
include an extension or tip which extends from the region  
10 underneath the gate electrode to a deeper source drain  
region.

In connection with P-type transistors, boron is  
commonly utilized for the deeper source drain junction.  
Boron diffuses more than N-type impurities because of  
15 transient enhanced diffusion (TED). The small size of the  
boron atom and its tendency to diffuse through interstitial  
motion results in increased diffusion. The transient  
enhanced diffusion of boron results in deeper and less  
highly doped P-typed source drain regions.

20 It is important to increase the doping density of the  
source drain extensions as device geometries shrink. This  
increase in density allows the P-type source drain  
extension resistivity to be reduced. Reducing the  
resistivity of the P-type extensions allows transistor  
25 drive current densities to scale appropriately so long as

the dose can be successfully activated during an anneal. The drive currents are directly related to the speed of the resulting transistors.

Conventionally, transient enhanced diffusion is  
5 counteracted by implanting fluorine just below the P-type source drain extension implant. During the first few milliseconds of activation anneal, the fluorine ties up the boron briefly, limiting its interstitial diffusion. This limiting of interstitial diffusion has the effect of  
10 reducing the boron diffusion by 10 to 20 percent after activation. This reduction of boron diffusion increases the dopant density and reduces the resistivity of the layer.

However, in order to further scale transistors, to  
15 improve the dopant density and resistivity, it would be desirable to further reduce the transient enhanced diffusion.

#### Brief Description of the Drawings

Figure 1 is a schematic, enlarged cross sectional view  
20 of one embodiment at an early stage of manufacture;

Figure 2 is a schematic, enlarged cross sectional view corresponding to Figure 1 at a subsequent stage of manufacture in accordance with one embodiment of the present invention;

Figure 3 is a schematic, enlarged cross sectional view of one embodiment of the present invention at a subsequent stage of manufacture;

Figure 4 is a concentration versus depth profile in accordance with one embodiment of the present invention; and

Figure 5 is a concentration versus depth profile in accordance with one embodiment of the present invention.

#### Detailed Description

Referring to Figure 1, a P-type transistor structure may include a deeper source drain region 18 and an implanted, shallower source drain region, tip or source drain extension 20 which may be formed, at least in part, by a boron implant. The implant is undertaken before the deeper regions 18 are formed, using the gate electrode 14 as a mask. The gate electrode 14 is formed over a dielectric layer 16 on a semiconductor substrate 12 in one embodiment of the present invention.

As shown in Figure 2, a deeper fluorine implant may be utilized to form the implanted region 24. The region 24 extends to a depth slightly below the depth of the boron source drain extension implant 20.

Next, a carbon implant may be utilized to form the implanted region 24 with both carbon and fluorine, as shown in Figure 3. In some embodiments, transient enhanced diffusion in the P-type source drain extension is reduced

when carbon is implanted in the same area as the fluorine at the same depth as or below the P-type source drain extension implant. In some embodiments as much as a 35 to 40 percent reduction in transient enhanced diffusion may be achieved. In some embodiments, the carbon species is implanted to the same depth as the fluorine species is normally implanted in the P-type source drain extension. As a result, the peak concentration of carbon and fluorine is immediately below the peak concentration of the boron implant in one embodiment.

Referring to Figure 4, implantation of carbon in conjunction with the standard fluorine implant significantly reduces the diffusion of the P-type boron source drain extension implant. For example, in FIGURE 4, the profile labeled "B w/C & F" shows a dopant profile of an annealed boron implant that had both carbon and fluorine implants slightly deeper than the boron implant. The presence of both the carbon and fluorine reduces the boron depth to 323 Angstroms at a concentration of  $1E18$  ions per  $cm^3$ .

The "B w/F only" profile had only a boron and fluorine implant. Note the deeper junction profile of the boron with fluorine protocol, demonstrating the increased TED. These Secondary Ion Mass Spectrometry (SIMS) profiles are all of the same Boron P-type extension implant after the activation spike anneal on a rapid thermal annealing tool.

In some embodiments, the carbon implant may be done at relatively low energy of about 6 KeV or less with a dose of about  $1E15$  ions/cm<sup>2</sup>. The fluorine implant may be at low energy, for example, of about 10 KeV or less with a dose of about  $1E15$  ions/cm<sup>2</sup>, in some embodiments of the present invention.

The reduction of transient enhanced diffusion may also be enhanced if an Arsenic Halo implant occurs before the P-type source drain extension implants. Referring to Figure 5, the SIMS profile labeled "after" indicates the Arsenic Halo implant was done after the extension implants. The profile labeled "before" indicates the Arsenic Halo implant was done prior to the extension implant. Decreased TED may be achieved by reversing the normal step order in some embodiments.

In some embodiments, germanium and/or silicon implants may also be used with boron, fluorine, and carbon implants.

While the present invention has been described with respect to a limited number of embodiments, those skilled in the art will appreciate numerous modifications and variations therefrom. It is intended that the appended claims cover all such modifications and variations as fall within the true spirit and scope of this present invention.